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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REVOCATION OF POWER OF ATTORNEY, NEW POWER OF ATTORNEY BY ASSIGNEE AND CHANGE OF CORRESPONDENCE ADDRESS

Sir:

Assignee hereby revokes all powers of attorney previously granted with respect to the patent applications identified in Appendix A, and appoints the firm of Myers Bigel Sibley & Sajovec:

Customer No. 20792

as its attorney, with full power of substitution and revocation to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all communications as follows:

Customer No. 20792

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Facsimile: (919) 854-1401

Assignee hereby elects under 37 C.F.R. § 3.71 to prosecute the patent applications listed in Appendix A.

The undersigned Assignee hereby certifies that Samsung Electronics Co., Ltd. is the assignee of the entire right, title, and interest in the patent applications identified in Appendix A by virtue of a chain of title from the inventor(s) of the patents or patent applications identified to Renesas Technology Corp. and then to the current assignee as shown in Appendix A.

The documents in the chain of title of the patent application identified above have been reviewed and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the Assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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Samsung	Electronics	C0.,	Lla

By: Dewn Jack Kong

Jeong-Tack Kong

Title: Senior Vice President of IP Team

Date: Tuly 1, 2008

7	Application	Patent	Filing Date	Assignment Recorded	Reel	Frame	Title of Patent
	No.	No.					
	08/841612	5,870,218	04/30/1997	Hitachi Ltd.	008541	0020	Non-volatile Semiconductor Memory Device Which Stores Multi-Value
				Renesas	019287	0688	Information
				Samsung	021172	0367	
	09/096457	5,982,667	06/11/1998	Renesas	019287	0746	Non-volatile Semiconductor Memory
				Samsung	021172	0367	Information By Controlling Erase And Plural Write States of Each Memory Cell
	09/339960	6,181,603	06/25/1999	Renesas	019287	0750	Non-volatile Semiconductor Memory Device Having Plural Memory Cells
				Samsung	021172	0367	Which Store Multi-Value Information
	09/715106	6,396,736	11/20/2000	Renesas	019287	0754	Nonvolatile Semiconductor Memory Device Which Stores Multi-Value
				Samsung	021172	0367	Information
	10/154853	6,771,537	05/28/2002	Renesas	014569	0585	Nonvolatile Semiconductor Memory
				Samsung	021172	0367	Device which Stores Multi-Value Information
Т.	10/832311	7,031,187	April 27, 2004	Rènesas	021134	0753	Nonvolatile Semiconductor Memory Device Which Stores Multi-Value
				Samsung	021172	0367	Information
T	11/332206	7,245,532	January 17, 2006	Samsung	021172	0367	Nonvolatile Semiconductor Memory Device Which Stores Multi-Value Information
	11/595880	7,394,697	November 13, 2006	Samsung	021172	0367	Nonvolatile Semiconductor Memory Device Which Stores Multi-Value
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	09/282204	angung ang ang ang ang ang ang ang ang ang a	08/885184		08/451268		08/422940		08/422941		08/179960	yang garanan da sabbasa	07/704739		12/117918	
	6,255,690		5,904,518		5,656,522		5,629,541		5,656,839		5,407,853		5,300,802	-	in	
	March 31, 1999		June 30, 1997		May 30, 1995		April 17, 1995		April 17, 1995		January 11, 1994		May 20, 1991		May 9, 2008	
Samsung	Renesas	Samsung	Renesas	Samsung	Renesas	Samsung	Renesas	Samsung	Renesas	Samsung	Renesas	Samsung	Renesas	Samsung	Renesas	
021172	021076	021172	021076	021172	021076	021172	021076	021172	021076	021172	021076	021172	021076	021172	021134	
0367	0845	0367	0845	0367	0845	0367	0845	0367	0845	0367	0845	0367	0845	0367	0584	
	Non-volatile Semiconductor Memory	Transistor Type Nonvolatile Memory Cells	Method of Manufacturing a	Having Single-Element Type Non-volatile Memory Elements	Method of Manufacturing a Semiconductor Integrated Circuit Device	Non-volatile Cells and Facilitated for Both Electrical Erasing and Writing of Data	Semiconductor Memory Device Constituted by Single Transistor Type	Memory Elements	Semiconductor Integrated Circuit Device Having Single-Element Type Nonvolatile	Element Type Non-Volatile Memory Elements	Method of Making Semiconductor Integrated Circuit Device Having Single-	Memory Elements	Semiconductor Integrated Circuit Device Having Single-Element Type Non-volatile	Information	Nonvolatile Semiconductor Memory Device Which Stores Multi-Value	Information

Deterioration Determining Function	0367	021172	Samsung	1999		
Semiconductor Memory Device Having	0845	021076	Renesas	November 2,	6,223,311	09/432389
	0367	021172	Samsung			
o	0845	021076	Renesas			
Semiconductor Memory Device Having Deterioration Determining Function	0596	008892	Hitachi, Ltd.	September 11,	5,978,941	08/913338
Memory Elements	0367	021172	Samsung	-		
Semiconductor Integrated Circuit Device Having Single-Element Type Non-volatile	0845	021076	Renesas	March 31, 2006	7,399,667	11/393774
Memory Elements	0367	021172	Samsung			
Semiconductor Integrated Circuit Device Having Single-Element Type Non-volatile	0845	021076	Renesas	September 8, 2005	7,071,050	11/220723
Cells and a Peripheral Circuit Portion with MISFETs						
Non-volatile Memory Cell Portion with Single MISEFT Transistor Type Memory	0367	021172	Samsung			
Method of Manufacturing a Semiconductor Memory Device Having a	0845	021076	Renesas	April 7, 2004	6,960501	10/819205
Memory Cell Portion Including MISFETs With a Floating Gate and a Peripheral Circuit Portion With MISFETs	0367	021172	Samsung			
Method of Manufacturing a Semiconductor Memory Device Having a	0585	014569	Renesas	June 10, 2002	6,777,282	10/164626
volatile Memory Cell Portion with Single Transistor Type Memory Cells and Peripheral Portion with MISFETs	0367	021172	Samsung			
Method of Manufacturing a Semiconductor Device Having Non-	0845	021076	Renesas	June 5, 2001	6,451,643	09/873451

Variable Capacity Semiconductor Memory	0262	010954	Mitsubishi	July 20, 2000	6,496,409	09/620719
o	0367	021172	Samsung			
Non-volatile Semiconductor Memory Device for Writing Multivalued Data	0869	016400	Renesas	June 25, 2007		11/819016
C	0367	021172	Samsung			
Nonvolatile Semiconductor Memory Device for Writing Multivalued Data	0869	016400	Renesas	June 25, 2007		11/819015
C	0367	021172	Samsung			
Nonvolatile Semiconductor Memory Device for Writing Multivalued Data	0869	016400	Renesas	March 18, 2005	7,242,611	11/082992
•	0367	021172	Samsung			
Method of Making an EPROM With Peripheral Transistor	0845	021076	Renesas	April 10, 1995	5,604,142	08/419232
	0367	021172	Samsung			
Method of Manufacturing a Semiconductor Device	0845	021076	Renesas	June 14, 1994	5,472,891	08/260229
	0367	021172	Samsung	- / / /		
Method of Manufacturing EEPROM Memory Device	0845	021076	Renesas	December 15,	5,340,760	07/992473
	0367	021172	Samsung	1001		
Semiconductor Memory Device	0845	021076	Renesas	September 24,	5,189,497	07/765065
	0367	021172	Samsung			
Semiconductor Memory Device	0845	021076	Renesas	April 30, 1990	5,079,603	07/517386
Q	0367	021172	Samsung			
Semiconductor Memory Device Having Deterioration Determining Function	0585	014569	Renesas	February 28, 2001	6,694,460	09/794073

Nonvolatile Memory and Method of Erasing for Nonvolatile Memory	0367	021172	Samsung	November 23,2005	7,072,224	11/284949
Elasing for Nonvoladic McHoly	0367	021172	Samsung	0002		
Nonvolatile Memory and Method of	0411	014682	Renesas	November 5,	7,068,541	10/700592
	0367	021172	Samsung			
Requirements	0409	021029	Renesas			
Non-volatile Memory Programming at	0104	007374	Hitachi	March 1, 1994	5,422,856	08/203303
	0367	021172	Samsung			
Device	0337	021118	Renesas			